

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	driving apparatus and display device and digital signal processing circuit and digital-to-analog converter and analog signal processing circuit and power supply circuit and predetermined power save	US-PGPUB	ADJ	ON	2006/09/29 16:24

Interference Search History  
Printout

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	driving apparatus and display device and digital signal processing circuit and digital-to-analog converter and analog signal processing circuit and power supply circuit and predetermined power save	US-PGPUB	ADJ	ON	2006/09/29 16:26
L2	27574	driv\$5 and display and digital signal proces\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:26
L3	9659	driv\$5 and display and digital signal proces\$6 and (digital near4 analog converter or D/A converter)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:28
L4	851	driv\$5 and display and digital signal proces\$6 and (digital near4 analog converter or D/A converter) and analog signal proces\$6	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:28
L5	313	driv\$5 and display and digital signal proces\$6 and (digital near4 analog converter or D/A converter) and analog signal proces\$6 and power supply	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:29
L6	1	driv\$5 and display and digital signal proces\$6 and (digital near4 analog converter or D/A converter) and analog signal proces\$6 and power supply and predetermined power save	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:31
L7	1	driv\$5 and display and digital signal proces\$6 and (digital near4 analog converter or D/A converter) and analog signal proces\$6 and power supply and predetermined power save and reduce\$5 and supply voltage	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/09/29 16:32